



BCH CODE ENCODER/DECODER TRAINER MODEL - BCH100

This trainer has been designed with a view to provide practical and experimental knowledge of BCH code Encoder/Decoder.

SPECIFICATIONS

1. Variable data rates up to 1.4 Mbps input.
2. Two commonly used (N, K, t) RS configurations.
(N, K, t) = (255, 233, 11)
(N, K, t) = (80, 56, 12)
3. Automatic frame synchronization.
4. Single 5V supply
5. Connectorized 3"x 3" module for ease of prototyping. Standard 40 pin 2mm dual row connectors (Left, right, bottom). Interfaces with 5V and 3.3V logic.
6. All parts are soldered on single PCB with complete circuit diagram Screen-printed in multi-Colour.

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.

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